Userspace RCU - Feature #940

Wire up sys membarrier on each architecture

09/26/2015 12:00 PM - Mathieu Desnoyers

Status:	New	**Start date:**	09/26/2015
Priority:	Normal	**Due date:**	
Assignee:	Mathieu Desnoyers	**% Done:**	0%
Category:		**Estimated time:**	0.00 hour
Target version:			

Description

History

#1 - 09/26/2015 12:01 PM - Mathieu Desnoyers

x86 32/64 is done.

#2 - 09/26/2015 12:01 PM - Mathieu Desnoyers

s390/s390x is done.

#3 - 09/26/2015 12:03 PM - Mathieu Desnoyers

For power8 and aarch64, will need to bind to arch-specific headers rather than "gcc", but still use the generic atomic implementation.

#4 - 09/26/2015 12:03 PM - Mathieu Desnoyers

powerpc 32/64 (except power8) is done.

#5 - 09/26/2015 12:04 PM - Mathieu Desnoyers

For ia64, will need to create arch-specific header, and still use gcc generic atomics.

#6 - 09/26/2015 12:17 PM - Mathieu Desnoyers

tile needs own arch-specific header, which use generic atomics.

#7 - 09/28/2015 10:45 AM - Mathieu Desnoyers

arm system call has been pulled into Linux. Should be added to urcu.

#8 - 09/28/2015 10:58 AM - Mathieu Desnoyers

arm32 is done.

#9 - 09/28/2015 11:19 AM - Mathieu Desnoyers

power8 is done.

#10 - 09/28/2015 11:31 AM - Mathieu Desnoyers

aarch64 is done.

#11 - 09/28/2015 11:34 AM - Mathieu Desnoyers

ia64 is done.

#12 - 09/28/2015 11:37 AM - Mathieu Desnoyers

tile is done.

#13 - 09/28/2015 01:04 PM - Mathieu Desnoyers

Architectures not yet available at kernel level but present in urcu: alpha, hppa, mips, sparc64.

#14 - 10/05/2015 05:12 PM - Mathieu Desnoyers

MIPS syscalls are merged into mainline kernel, however since there are 3 mips ABIs, wiring them up in urcu is not straightforward and would require

access to MIPS boards for testing.

#15 - 11/16/2015 11:33 AM - Mathieu Desnoyers

hppa is done.

#16 - 11/16/2015 11:34 AM - Mathieu Desnoyers

sparc64 is done.

#17 - 11/16/2015 11:42 AM - Mathieu Desnoyers

Still missing: MIPS (there are about 3 ABIs, not trivial, need testing). alpha (syscall not wired in the kernel).